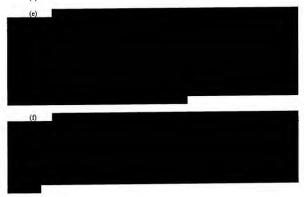
PTOISB96 (11-08)
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STATEMENT UNDER 37 CFR 3.73(b)							
Applicant/Patent Owner:	Aptina Imaging Corporation						
Application No./Patent No.	7,154,136	Filed/Issue Date:	December 26, 2006				
	ISOLATION STRUCTURES FOR PREVENTING PHOTONS AND CARRIERS FROM REACHING ACTIVE AREAS AND METHODS OF FORMATION						
Aptina Imagi (Name of Assignee)	Aptina Imaging Corporation , a Corporation (Type of Assignee, e.g., corporation, perinership, university, government agency, etc.)						
states that it is:							
the assignee of	f the entire right, title, and interest; of	or					
2. an assignee of	less than the entire right, title and in	nterest.					
(The extent (by	percentage) of its ownership intere	est is					
in the patent application/p	atent identified above by virtue of ei	ther:					
recorded in the U	om the inventor(s) of the patent app inited States Patent and Trademark	Office at Reel	pove. The assignment was				
	, or for which a copy there	of is attached.					
OR							
	n the inventor(s), of the patent applica		•				
	an G. Cole et al. nent was recorded in the United S	To: Micron Techr					
	015010 , Frame0537						
2. From: Mic	ron Technology, Inc.	To: Aptina Imagir	ng Corporation				
	nent was recorded in the United S						
Reel	, Frame	, or for which a copy	thereof is attached.				
3. From:		To:					
	nent was recorded in the United S						
Reel, Frame, or for which a copy thereof is attached.							
Additional documents in the chain of title are listed on a supplemental sheet.							
X As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.							
[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]							
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.							
	Signature		ecember 16, 2008				
_							
	homas J. D'Amico inted or Typed Name		(202) 420-2232 Telephone Number				
	**		p				
A	torney for Assignee Title						

PATENT ASSIGNMENT AGREEMENT

1. Definitions

- (a) "Agreement" shall mean this Patent Assignment Agreement.
- (b) "Assignee" shall mean Aptina Imaging Corporation, a Cayman Islands corporation with offices at c'o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.
- (c) "Assignor" shall mean Micron Technology, Inc., a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.
 - (d) "Effective Date" shall mean October 3, 2008.



- (g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

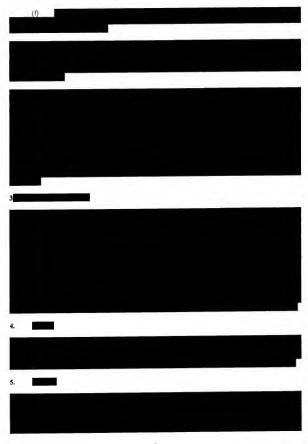


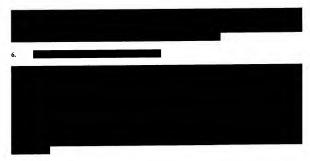
- (a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.
- (b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States, and expressed the Imaging Patent Applications to Assignee.



- (d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to subset for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee.
- (e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any,

 Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.





7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

[the rest of this page intentionally left blank]

	* .	
	Micron Technology, Inc.	
	Signed: Signed:	
	Date: 2/20/42	2008
Notarization State of Idaho		
County of Ada	cl v	1 11
On 9/26/08, before me, far 14 D. Sorry	, personally appeared the above-named	Appleton
who executed this Assignment in my presence and ack	nowledged to me that he did so of his own free will a	nd in his
authorized capacity for the purposes set forth herein.	Signed:	
REVIEWED MTI LEGAL	My commission expires: \\ \frac{\fir}{\fir}}}}}}{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac	
C. Summing R. S.	Agreed to by:	
TAD TAD TAD	Aptina Imaging Corporation	
N A	Signed: Thomas I- Jung / -	
WHITE CONTRACTOR OF THE PROPERTY OF THE PROPER	Signed:	
NA THE STATE OF TH	Date: 9/26/09	2008
Notarization OF		
Notarization State of Idaho		
County of Ada		
On 9/26/08, before me, Parla R. Paras (botaly same)		
who executed this Assignment in my presence and ackreauthorized capacity for the purposes set forth herein.	nowledged to me that he did so of his own free will a	nd in his
aumorized capacity for the purposes set forth detent.	A STATE OF THE STA	
A R. S.	Signed Signed	
A R. So	My commission expires:	
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Agreed to by:

[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A" IMAGING PATENTS

Aptina Issued

PATENT NUMBER	FILE NUMBER		COUNTRY NAME	ISSUE DATE
7133076	PB(T-6066:00/US	CONTOURED SURFACE COVER PLATE FOR IMAGE SENSOR ARRAY	United States of America	Nov 7, 2006
7133553	AVGO-0048.00/US	CORRELATION-BASED COLOR MOSAIC INTERPOLATION ADJUSTMENT USING LUMINANCE GRADIENTS	United States of America	Nov 7, 2005
7136157	2002-1174,000US	METHOD AND APPARATUS FOR TESTING IMAGE SENSORS	United States of America	Nev 14, 2006
7141841	2003-0105.00/US	MAGE SENSOR HAVING A TRANSISTOR FOR ALLOWING INCREASED DYNAMIC RANGE	United States of America	Nov 28, 2006
7192224	2002-1365 00/US	METHOD FOR MISMATCH DETECTION, BETWEEN THE REGUENCY OF LUMINATION SOURCE AND THE DURATION OF OPTICAL INTEGRATION TIME FOR MASER SWITTH ROLLING SHUTTER	United States of America	NeV28, 2006
7145189	2004-0296,00/US	PHOTON AMPLIFICATION FOR IMAGE SENSORS	United States of America	Dec 5, 2006
7148462 7148526	2003-0714-01/US 2003-0106-00/US	PIXEL WITH DIFFERENTIAL READOUT. PINNED PHOTODIODE STRUCTURE AND METHOD OF FORMATION	United States of America	Dec 12, 2006 Dec 12, 2006
7148833	2005-0485.00/US	SHARING OPERATIONAL SHARING OPERATIONAL AMPLIFIER BETWEEN TWO STAGES OF RIPELINED ADC AND/OR TWO CHANNELS OF SIGNAL PROCESSING GIRCUITRY	United States of America	Dec 12, 2006
7151472	PBIT-0158.01/US	REFERENCE VOLTAGE STABILIZATION IN CMOS SENSORS	United States of America	Dec 19, 2006
7/51476	v 2003/4192.00/US	MINIMIZED DIFFERENTIAL SAR TYPE COLUMN MIDE ADD FOR EMOS MAGE SENSORS	Unsed States of America	Dac-19, 2006
7153719	2004-0396.00/US	METHOD OF FABRICATING A STORAGE GATE PIXEL DESIGN	United States of America	Dec 26, 2006
7154078 174	2002-0330.01/US	SCHEME FOR CHUS APS PIXELS	United States of America	Dec 28: 2006
7154136	2003-0343.00/US	ISOLATION STRUCTURES FOR PREVENTING PHOTONS AND CARRIERS FROM REACHING ACTIVE AREAS AND METHODS OF	United States of America	Dec 26, 2006

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